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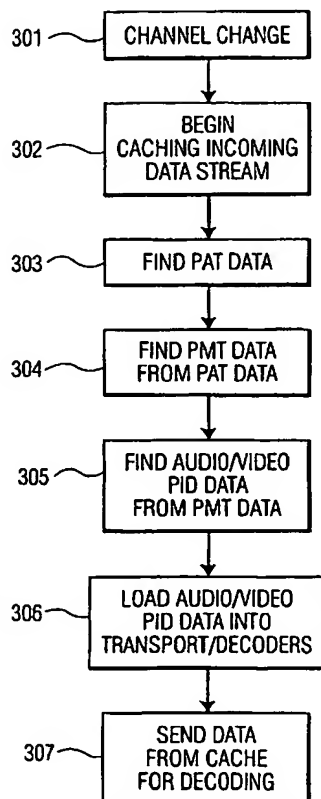
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(54) Title: APPARATUS AND METHOD FOR DATA CACHING TO REDUCE CHANNEL CHANGE DELAY



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(57) Abstract: An apparatus (100) performs a data caching operation responsive to a channel change event which reduces channel change times. According to an exemplary embodiment, the apparatus (100) includes a cache memory (103) operative to cache an incoming data stream responsive to a channel change command. A processor (101) is operative to find program specific information (PSI) included within the incoming data stream. A decoder (104) is operative to decode the cached data stream responsive to the processor (101) finding the program specific information.

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## INTERNATIONAL SEARCH REPORT

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## A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H04N5/00 H04N7/24 H04N5/44

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, INSPEC

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 00 57646 A (TAUNTON MARK ; ELEMENT 14 INC (US)) 28 September 2000 (2000-09-28) abstract page 2, line 28 - page 3, line 18 page 9, line 8 - page 10, line 9 ---	1-20
A	WO 99 09741 A (NEXT LEVEL COMM) 25 February 1999 (1999-02-25) section "Summary of the invention" ---	1-20
A	US 6 118 498 A (REITMEIER GLENN ARTHUR) 12 September 2000 (2000-09-12) abstract column 6, line 21 - line 54 ---	1-20
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☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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